



Top 6 CS Program

In both U.S. News & World Report and CS Rankings

Efficient and Intelligent Computing (EIC) Lab @ Georgia Tech. Seek for Highly-Motivated Interns Working on Efficient Machine Learning Hardware



Intern Alumni

J. Zhang

- Next move: PhD@UW
 - 3rd Author DAC'23
 - Co-Author MICRO'24 (Best Paper Award)
 - Co-Author HPCA'25

Z. Ye

- Next move: PhD@Georgia Tech

W. Zhu

- Next move: PhD@USC
 - Co-Author ICCAD'22
 - 3rd Author ISCA'23
 - Co-Author MICRO'24 (Best Paper Award)



Why us?

- Pioneered research in efficient AI hardware (architecture, FPGA, chip, etc.)
- Regularly publish in top-tier hardware conferences (e.g., ISCA/MICRO/HPCA/DAC/ICCAD)



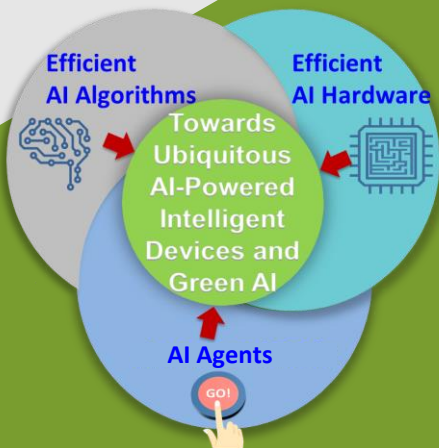
Why you?

- Self-motivated and passionate about research
- Knowing FPGA/HDL/HLS and popular architectures of ML accelerators
- Experience in implementation of some basic components (e.g., Conv, GEMM)



What could you gain?

- Opportunities to **work in cutting-edge research projects** and **publish papers** in top-tier conferences and journals
- **Collaborations with big tech companies**, such as NVIDIA/Meta/IBM/Intel/Google
- Strong **recommendation** for your MS/PhD/job application



Recent Projects (Click to view more information)

- ❖ AI Agents for Hardware: [GPT4AIGChip](#) [MG-Verilog](#)
- ❖ Efficient 3D Intelligence: [Instant-3D](#) [Fusion-3D](#)
- ❖ Efficient VR/AR Camera: [EyeCoD](#)
- ❖ Efficient Transformer: [ViTCoD](#) [ViTALiTy](#)

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